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In 2006, Atmel released microcontrollers based on the 32-bit AVR32 architecture. This was a completely different architecture unrelated to the 8-bit AVR, intended to compete with the ARM-based processors. It had a 32-bit data path, SIMD and DSP instructions, along with other audio- and video-processing features. The instruction set was similar to other RISC cores, but it was not compatible with ...

[8051 Microcontroller Notes - SlideShare](#)

MCS 51 (8051 family) and PIC microcontrollers are based on Harvard architecture. iii. Microprocessor with Microcontroller Microprocessor Microcontroller Microprocessor is complete functional CPU i.e. it contains ALU, registers, stack pointer, program counter, instruction decode and control unit and interrupt processing circuits.

[Intel MCS-51 - Wikipedia](#)

Reading. The AVR Microcontroller and Embedded Systems using Assembly and C by Muhammad Ali Mazidi, Sarmad Naimi, and Sepehr Naimi. Sections: 9.1, 9.3

[\(PDF\) Smart traffic light control system](#)

Reading. The AVR Microcontroller and Embedded Systems using Assembly and C by Muhammad Ali Mazidi, Sarmad Naimi, and Sepehr Naimi. Sections: 10.3, 10.5